

## WEST Search History

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DATE: Wednesday, January 05, 2005

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		<i>DB=USOC; PLUR=YES; OP=ADJ</i>	
<input type="checkbox"/>	L32	(interrupt\$4 adj signal) and (compar\$4 or detect\$4) same chang\$ and ((data adj frame\$) same signal)	3
		<i>DB=DWPI; PLUR=YES; OP=ADJ</i>	
<input type="checkbox"/>	L31	(interrupt\$4 adj signal) and (compar\$4 or detect\$4) same chang\$ and ((data adj frame\$) same signal)	1
		<i>DB=TDBD; PLUR=YES; OP=ADJ</i>	
<input type="checkbox"/>	L30	(interrupt\$4 adj signal) and (compar\$4 or detect\$4) same chang\$ and ((data adj frame\$) same signal)	1
		<i>DB=USPT,EPAB; PLUR=YES; OP=ADJ</i>	
<input type="checkbox"/>	L29	(interrupt\$4 adj signal) and (compar\$4 or detect\$4) same chang\$ and ((data adj frame\$) same signal)	174
<input type="checkbox"/>	L28	(interrupt\$4 adj signal) and (compar\$4 or detect\$4) same chang\$	7460
		<i>DB=USPT; PLUR=YES; OP=ADJ</i>	
<input type="checkbox"/>	L27	L26 and l5	7
<input type="checkbox"/>	L26	L24 and l1	8
<input type="checkbox"/>	L25	L24 and 709/2\$\$ccls.	0
<input type="checkbox"/>	L24	l2 and L23	8
<input type="checkbox"/>	L23	L22 and 370/4\$\$ccls.	198
<input type="checkbox"/>	L22	(interrupt\$4 adj signal) and (compar\$4 or detect\$4) same chang\$	7450
<input type="checkbox"/>	L21	L19 and L1 and L2	3
<input type="checkbox"/>	L20	L13 and L19	0
<input type="checkbox"/>	L19	(370/389).ccls. or 370/405.ccls or 370/4\$\$ccls	1165
<input type="checkbox"/>	L18	L6 and L17	14
<input type="checkbox"/>	L17	(compar\$4 or detect\$4) same chang\$	387965
<input type="checkbox"/>	L16	6633573.pn.	1
<input type="checkbox"/>	L15	protect\$4 and L14	6
<input type="checkbox"/>	L14	switch\$4 and L13	7
<input type="checkbox"/>	L13	(interrupt adj signal) and L12	7
<input type="checkbox"/>	L12	type\$ and L11	8
<input type="checkbox"/>	L11	16 and L9	8
<input type="checkbox"/>	L10	16 and L9	8
<input type="checkbox"/>	L9	(fpga or field programmable gate array) and interval\$ and L7	8

<input type="checkbox"/>	L8	(fpga or field programmable gate array) and L7	8
<input type="checkbox"/>	L7	interrupt\$4 and L6	28
<input type="checkbox"/>	L6	L1 and L5	28
<input type="checkbox"/>	L5	(backplane or back-plane) and L4	28
<input type="checkbox"/>	L4	serial\$4 and L3	35
<input type="checkbox"/>	L3	(upstream or up-stream or up stream) and (down stream or downstream or down-stream) and L2	37
<input type="checkbox"/>	L2	((data adj frame\$) same signal) and L1	102
<input type="checkbox"/>	L1	(sonet or synchronous optical network) and (ram or random access memory)	1048

END OF SEARCH HISTORY